



## COURSE OUTLINE

# ELC201 Analog Electronic Circuits

**Course Coordinator:** Kenneth Ang (lang@usc.edu.au) **School:** School of Science, Technology and Engineering

2021 | Semester 1

USC Moreton Bay

**ON CAMPUS**

Most of your course is on campus but you may be able to do some components of this course online.

*Please go to the USC website for up to date information on the teaching sessions and campuses where this course is usually offered.*

## 1. What is this course about?

### 1.1. Description

Electronic components are embedded into devices to make our lives easier, better and smarter. In this course you will develop your understanding of the theory, design and applications of analog circuits and electronic devices. Through a series of application-oriented topics, you will expand your foundational concepts and skills to be able to design and implement electronic-based devices and engineering systems. You will actively learn the content and skills through a series of scaffolded lectures, guest lectures, tutorials and laboratory tasks.

### 1.2. How will this course be delivered?

ACTIVITY	HOURS	BEGINNING WEEK	FREQUENCY
<b>ON CAMPUS</b>			
<b>Lecture</b>	2hrs	Week 1	13 times
<b>Tutorial/Workshop</b>	2hrs	Week 2	5 times
<b>Laboratory</b>	2hrs	Week 1	5 times

### 1.3. Course Topics

- Introduction to Devices and Circuits
- Semiconductors
- Diodes
- Field Effect Transistors
- Bipolar Junction Transistors
- Transistor Amplifiers
- Differential and Multistage Amplifiers
- Filters
- Oscillators

## 2. What level is this course?

200 Level (Developing)

Building on and expanding the scope of introductory knowledge and skills, developing breadth or depth and applying knowledge and skills in a new context. May require pre-requisites where discipline specific introductory knowledge or skills is necessary. Normally, undertaken in the second or third full-time year of an undergraduate programs.

### 3. What is the unit value of this course?

12 units

### 4. How does this course contribute to my learning?

COURSE LEARNING OUTCOMES		GRADUATE QUALITIES
On successful completion of this course, you should be able to...		Completing these tasks successfully will contribute to you becoming...
1	Use electronic circuit elements to design and build a range of electronic circuits.	Empowered
2	Apply fundamental circuit theories and understanding of mathematical principles to the operation of electronic circuits.	Creative and critical thinker
3	Demonstrate practical skills and application of theories, and practice in electronics engineering.	Knowledgeable
4	Communicate the operation of circuits using appropriate engineering terminology, symbols and diagrams	Engaged
5	Work collaboratively in teams to design electronics circuits to meet specified requirements	Empowered

### 5. Am I eligible to enrol in this course?

Refer to the [USC Glossary of terms](#) for definitions of “pre-requisites, co-requisites and anti-requisites”.

#### 5.1. Pre-requisites

ENG103 and must be enrolled in Program SC404, SC405, SC410, SC411, SC425, SC305 or AB101

#### 5.2. Co-requisites

Not applicable

#### 5.3. Anti-requisites

Not applicable

#### 5.4. Specific assumed prior knowledge and skills (where applicable)

A good understanding of the content of MTH103 and MTH104

### 6. How am I going to be assessed?

#### 6.1. Grading Scale

Standard Grading (GRD)

High Distinction (HD), Distinction (DN), Credit (CR), Pass (PS), Fail (FL).

#### 6.2. Details of early feedback on progress

Early and regular feedback will be provided by discussing exemplars in class, having structured formative question and answer sessions and other activities related to the tasks, and receiving specific feedback on draft submissions periodically to ensure your work progresses in an informed way.

#### 6.3. Assessment tasks

DELIVERY MODE	TASK NO.	ASSESSMENT PRODUCT	INDIVIDUAL OR GROUP	WEIGHTING %	WHAT IS THE DURATION / LENGTH?	WHEN SHOULD I SUBMIT?	WHERE SHOULD I SUBMIT IT?
All	1	Portfolio	Group	30%	1500 words equivalent	Week 4	Online Assignment Submission with plagiarism check
All	2	Artefact - Technical and Scientific, and Written Piece	Individual	30%	5 pages each including figures and diagrams	Refer to Format	Online Assignment Submission
All	3	Examination	Individual	40%	2 hours	Exam Period	Exam Venue

### All - Assessment Task 1: Report-based Workshop Portfolio

<b>GOAL:</b>	These tasks will develop your ability to: design, build and operate simple circuits; analyse data obtained from the experiments and compare and use industrial practices to document your conclusions in a portfolio of engineering reports.	
<b>PRODUCT:</b>	Portfolio	
<b>FORMAT:</b>	You will work as a group to produce a portfolio of electronic circuit and system designs in the form of a document approximately 1000 words equivalent in length with figures, text, and diagrams. You will provide drafts of your work in Weeks 1-3 and receive formative feedback on your progress.	
<b>CRITERIA:</b>	<b>No.</b>	<b>Learning Outcome assessed</b>
	1	Utilization of electronics circuit elements to design and build a range of electronic circuits.
	2	Demonstration of the operation and performance characteristics of circuits and systems.
	3	Communication of the operation of the circuits using appropriate engineering terminology, symbols and diagrams.
	4	Demonstrated ability to work collaboratively in teams to design electronics circuits system to meet specified requirements
	5	Engineers Australia competencies assessed in this task: 2.3 Application of systematic engineering synthesis and design processes within the technology domain
	6	Engineers Australia competencies assessed in this task: 3.2 Effective oral and written communication in professional and lay domains.
	7	Engineers Australia competencies assessed in this task: 3.6 Effective team membership and team leadership
	8	Assessment criteria are mapped to the course learning outcomes <span style="float: right;">1 2 3 4 5</span>

### All - Assessment Task 2: Problem-based Assignment

<b>GOAL:</b>	These assignments will develop your ability to extend and apply your theoretical knowledge by identifying and documenting solutions to practical problems .	
<b>PRODUCT:</b>	Artefact - Technical and Scientific, and Written Piece	
<b>FORMAT:</b>	For each assignment, you are required to work individually to produce written solutions in the form of a document approximately 2000 words equivalent in total with figures, text, and diagrams. Due Week 6, 8, 12	
<b>CRITERIA:</b>	<b>No.</b>	<b>Learning Outcome assessed</b>
	1	Utilization of electronic circuit elements to design and build a range of electronic circuits.
	2	Application of theories to solve problems of electronic circuits and systems.
	3	Demonstration of understanding through use of correct formulae; and
	4	Communication of the operation of the circuits using appropriate engineering terminology, symbols and diagrams.
	5	Engineers Australia competencies assessed in this task: 1.3 In-depth understanding of specialist bodies of knowledge within the engineering discipline
	6	Engineers Australia competencies assessed in this task: 2.1 Application of established engineering methods to complex engineering problem solving.

## All - Assessment Task 3: Final Exam

<b>GOAL:</b>	The final exam will develop your ability to independently apply your skills and knowledge to solve familiar problem-based questions with confidence within a set time limit and without access to additional resources.	
<b>PRODUCT:</b>	Examination	
<b>FORMAT:</b>	Centrally scheduled handwritten 2-hour closed book examination.	
<b>CRITERIA:</b>	<b>No.</b>	<b>Learning Outcome assessed</b>
	1	Correct application of theory and knowledge to solve electronic circuit- problems
	2	Communication of the operation of the circuits using appropriate engineering terminology, symbols and diagrams
	3	Engineers Australia competencies assessed in this task: 1.1 Comprehensive, theory-based understanding of the underpinning natural and physical sciences and the engineering fundamentals applicable to the engineering discipline
	4	Engineers Australia competencies assessed in this task: 1.5 Knowledge of contextual factors impacting the engineering discipline. 2.2 Fluent application of engineering techniques, tools and resources.
	5	Engineers Australia competencies assessed in this task: 2.1 Application of established engineering methods to complex engineering problem solving.

## 7. Directed study hours

A 12-unit course will have total of 150 learning hours which will include directed study hours (including online if required), self-directed learning and completion of assessable tasks. Directed study hours may vary by location. Student workload is calculated at 12.5 learning hours per one unit.

## 8. What resources do I need to undertake this course?

Please note: Course information, including specific information of recommended readings, learning activities, resources, weekly readings, etc. are available on the course Blackboard site– Please log in as soon as possible.

### 8.1. Prescribed text(s) or course reader

Please note that you need to have regular access to the resource(s) listed below. Resources may be required or recommended.

REQUIRED?	AUTHOR	YEAR	TITLE	PUBLISHER
Required	A.S. Sedra, K.C. Smith, T.C. Carusone, V. Gaudet	2019	Microelectronic Circuits	n/a

### 8.2. Specific requirements

Fully enclosed shoes must be worn in the engineering laboratory. If you do not have the correct shoes you will not be allowed to do the practical. You must also undertake the laboratory induction before you can undertake any practical.

## 9. How are risks managed in this course?

Health and safety risks for this course have been assessed as low. It is your responsibility to review course material, search online, discuss with lecturers and peers and understand the health and safety risks associated with your specific course of study and to familiarise yourself with the University's general health and safety principles by reviewing the [online induction training for students](#), and following the instructions of the University staff.

## 10. What administrative information is relevant to this course?

### 10.1. Assessment: Academic Integrity

Academic integrity is the ethical standard of university participation. It ensures that students graduate as a result of proving they are competent in their discipline. This is integral in maintaining the value of academic qualifications. Each industry has expectations and standards of the skills and knowledge within that discipline and these are reflected in assessment.

Academic integrity means that you do not engage in any activity that is considered to be academic fraud; including plagiarism, collusion or outsourcing any part of any assessment item to any other person. You are expected to be honest and ethical by completing all work yourself and indicating in your work which ideas and information were developed by you and which were taken from others. You cannot provide your assessment work to others. You are also expected to provide evidence of wide and critical reading, usually by using appropriate academic references.

In order to minimise incidents of academic fraud, this course may require that some of its assessment tasks, when submitted to Blackboard, are electronically checked through SafeAssign. This software allows for text comparisons to be made between your submitted assessment item and all other work that SafeAssign has access to.

## 10.2. Assessment: Additional Requirements

### Eligibility for Supplementary Assessment

Your eligibility for supplementary assessment in a course is dependent of the following conditions applying:

The final mark is in the percentage range 47% to 49.4%

The course is graded using the Standard Grading scale

You have not failed an assessment task in the course due to academic misconduct

## 10.3. Assessment: Submission penalties

Late submission of assessment tasks may be penalised at the following maximum rate:

- 5% (of the assessment task's identified value) per day for the first two days from the date identified as the due date for the assessment task.

- 10% (of the assessment task's identified value) for the third day

- 20% (of the assessment task's identified value) for the fourth day and subsequent days up to and including seven days from the date identified as the due date for the assessment task.

- A result of zero is awarded for an assessment task submitted after seven days from the date identified as the due date for the assessment task. Weekdays and weekends are included in the calculation of days late. To request an extension you must contact your course coordinator to negotiate an outcome.

## 10.4. Study help

For help with course-specific advice, for example what information to include in your assessment, you should first contact your tutor, then your course coordinator, if needed.

If you require additional assistance, the Learning Advisers are trained professionals who are ready to help you develop a wide range of academic skills. Visit the [Learning Advisers](#) web page for more information, or contact Student Central for further assistance: +61 7 5430 2890 or [studentcentral@usc.edu.au](mailto:studentcentral@usc.edu.au).

## 10.5. Wellbeing Services

Student Wellbeing provide free and confidential counselling on a wide range of personal, academic, social and psychological matters, to foster positive mental health and wellbeing for your academic success.

To book a confidential appointment go to [Student Hub](#), email [studentwellbeing@usc.edu.au](mailto:studentwellbeing@usc.edu.au) or call 07 5430 1226.

## 10.6. AccessAbility Services

Ability Advisers ensure equal access to all aspects of university life. If your studies are affected by a disability, learning disorder mental health issue, injury or illness, or you are a primary carer for someone with a disability or who is considered frail and aged, [AccessAbility Services](#) can provide access to appropriate reasonable adjustments and practical advice about the support and facilities available to you throughout the University.

To book a confidential appointment go to [Student Hub](#), email [AccessAbility@usc.edu.au](mailto:AccessAbility@usc.edu.au) or call 07 5430 2890.

## 10.7. Links to relevant University policy and procedures

For more information on Academic Learning & Teaching categories including:

- Assessment: Courses and Coursework Programs
- Review of Assessment and Final Grades
- Supplementary Assessment
- Administration of Central Examinations
- Deferred Examinations
- Student Academic Misconduct
- Students with a Disability

Visit the USC website: <http://www.usc.edu.au/explore/policies-and-procedures#academic-learning-and-teaching>

## 10.8. General Enquiries

### In person:

- **USC Sunshine Coast** - Student Central, Ground Floor, Building C, 90 Sippy Downs Drive, Sippy Downs
- **USC Moreton Bay** - Service Centre, Ground Floor, Foundation Building, Gympie Road, Petrie
- **USC SouthBank** - Student Central, Building A4 (SW1), 52 Merivale Street, South Brisbane
- **USC Gympie** - Student Central, 71 Cartwright Road, Gympie
- **USC Fraser Coast** - Student Central, Student Central, Building A, 161 Old Maryborough Rd, Hervey Bay
- **USC Caboolture** - Student Central, Level 1 Building J, Cnr Manley and Tallon Street, Caboolture

**Tel:** +61 7 5430 2890

**Email:** [studentcentral@usc.edu.au](mailto:studentcentral@usc.edu.au)